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APPLICATION NO. FILING DATE		FIRST NAMED INVE					CONFIRMATION NO.
10773,680 12/16/2003 Tokahiso Nakano 246575US6 3581 TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT APPARATUS AND CIRCUIT BOARD AND INFORMATION READOUT METHOD							
APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE I	DUE PREV. PAID 15	SUE FEE	TOTAL FEE(S) DU	E DATE DUE
nonprovisional	NO	\$1440	\$300	\$0		\$1740	01/25/2008
EXAMINER		ART UNIT	CLASS-SUBCLASS	3			
FRANKLIN, RICHARD B		2181	710-005000				
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3. ASSIGNEE NAME AN PLEASE NOTE: Unles recordation as set forth (A) NAME OF ASSIGN Sony Corp	is an assigned is identi in 37 CFR 3.11. Comp NEE			he patent. If an ass g an assignment.		TRY)	document has been filed for
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 Change In Entity Statu a. Applicant claims 			D		4411 FX	ITITY status. Sec 37	OED 1 27/-1/21
							the assignee or other party in
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